INFORMATION
DISCLOSURE
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Applicant(s): Ryota Nanjo et al

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Evami	2	Macument No.	Name	Date	Class

Examination Initial	& TRAOS	cument No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
<u>IMI</u>		5,956,603	Talwar et al	9/21/99	438	520	
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FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Translation (Yes or No)
SMS	AF	2001-007220	1/12/01	JP	Yes, abstract
	AG	2000-269491	9/29/00	JP	Yes, abstract
	AH	2000-196037	7/14/00	JP	Yes, abstract
	ΑI	07-231044	8/29/95	JP	Yes, abstract
	AJ	06-053157	2/25/94	JP	Yes, abstract
JMJ	AK	05-067776	3/19/93	JP	Yes, abstract

OTHER DOCUMENTS

JMJ	AL	"Activation and Deactivation of Laser Thermal Annealed Boron, Arsenic, Phosphorus and Antimony Ultra-Shallow Abrupt Junctions", Murto et al; TH-13; Ion
<u>Jm1</u>	AM	Implantation Technology 2000 "Self-Aligned Nickel-Mono-Silicide Technology for High-Speed Deep Submicrometer Logic CMOS USI", Morimoto et al; Vol 42, No. 5, May 1995 IEEE,
<u>JMJ</u>	AN	pp 915-923. "70nm MOSFET with Ultra-shallow, Abrupt, and Super-doped S/D Extension 2
<u>IM J</u>	AO	Implemented by Laser Thermal Process (LTP); Bin Yu et al; 1999 EEE 7 7 "Front End Processes"; International Technology Roadmap for Semiconductors 1999; pp 105-141
Examiner	JM	Date Considered 10-30-03 2